REMARKS

Claims 1-2, 7-12, and 33-40 are pending in this application. In response to the' Advisory Action dated November 26, 2004, Applicant respectfully disagrees. Applicant can find no specific passages in either Murakami or Dohjo that motivate combining these references with Gu, or, for the reasons presented in the amendment dated October 28, 2004, that teach or suggest the claimed features of claims 1 and 33. Therefore, Applicant urges that the combination of Murakami or Dohjo with Gu would have been based on hindsight gained from Applicant's disclosure, and is improper. Accordingly, the Examiner's reconsideration of the rejection of claims 1-2, 7-12, and 33-40 is respectfully requested.

Applicant has added new claims 41-56 to further define the invention. New claims 41 and 49 disclose a thin film transistor array substrate that includes, *inter alia*, "a first pixel electrode . . . connected to [a] drain electrode through [a] first contact hole". None of the cited references disclose such a feature. Applicant urges that claims 1-2, 7-12, and 33-40, and new claims 41-56, are in condition for allowance. Early and favorable action on this case is respectfully requested.

Respectfully submitted,

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